

Fast Logic

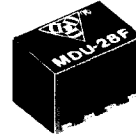
Multiple Digital Delay Units

SERIES: MDU-28F
(8 pins DIP) TTL Interfaced



Features:

- Auto-insertable.
- Low cost.
- Completely interfaced for TTL.
- Low profile.
- Fits standard 8 pins DIP socket.
- 2 equal delay units/package.



Specifications:

- **Delay tolerance:** See table.
- **Rise-time:** 2 ns typically.
- **Temperature coefficient:** 100 PPM/°C.
- **Temperature range:** 0° to + 70°C.
- **Input pulse width:** Min. 100% of total delay.
- **Input rep. rate:** Min. 3 x pulse width.
- **Supply voltage:** 5 Vdc ± 5%.
- **Supply Current:**
I_{ccL}: 54 ma.
I_{ccH}: 12 ma.
- **DC parameters:** See TTL-Fast Schottky Logic Table on Page 6.

Part No.	Delay Per Line (ns)
MDU-28F-6	6 ± 1
MDU-28F-8	8 ± 2
MDU-28F-10	10 ± 2
MDU-28F-12	12 ± 2
MDU-28F-16	16 ± 2
MDU-28F-20	20 ± 3
MDU-28F-30	30 ± 3
MDU-28F-35	35 ± 3
MDU-28F-40	40 ± 3
MDU-28F-45	45 ± 3
MDU-28F-50	50 ± 3
MDU-28F-60	60 ± 3
MDU-28F-75	75 ± 4
MDU-28F-100	100 ± 5
MDU-28F-125	125 ± 6.5
MDU-28F-150	150 ± 7.5
MDU-28F-175	175 ± 8
MDU-28F-200	200 ± 10
MDU-28F-250	250 ± 12.5

Test Conditions:

- Input Pulse Width: >150% of total delay.
- Time delay measured @ 1.5 V on rising edge.
- Rise-time is measured from .75 V to 2.4 V of leading time.
- All measurements made @ V_{cc} = 5V; T_A = +25°C; no loads on taps.

